

Quantitative Metrics for Defect Tolerance in Semiconductors

What is Defect Tolerance?

Defect tolerance¹ is the "resilience" of transport properties (carrier mobility and lifetime) to the presence of crystalline defects. It is a critical characteristic for materials amenable to low-cost manufacturing techniques, as these techniques typically introduce more defects in the nano- and microstructure.



According to **Shockley-Read-Hall statistics**, defects that introduce energy states that are "deep" in the bandgap (close to the middle) cause more recombination and hence are more detrimental to carrier lifetime than those that are "shallow" (near conduction or valence band edges).

The **hybrid halide perovskites** are a recent example of an extraordinarily defect-tolerant class of materials. We aim to learn from them to design similarly defect-tolerant materials that may be more stable and nontoxic.

Prior Understanding



Inspired by the perovskites' electronic structure, we previously developed² screening criteria for defect tolerance focused on proxies that are **computationally accessible** from density functional theory calculations:

Criterion	Outcome	Computational Accessibi
Large dielectric constant	Screen charged defects	Directly calculate
Small effective mass	Large mobility	Good approximation from
Antibonding VBM	Shallow defects	<i>s</i> -fraction, presence of pa oxidized cation



References

- 1. Zakutayev, A., et al. (2014). Defect tolerant semiconductors for solar energy conversion. Journal of Physical Chemistry Letters, 5(7), 1117–1125.
- *Communications*, 5(2), 265–275.

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m DOS artially

Improved Understanding

An intrinsic defect (such as a vacancy) introduces **new interactions** between orbitals on neighboring atoms that would otherwise have been bonded to the atom in the vacant site.

Pbl₂ (no vacancy)





Pbl₂ with V₁



To design a defect-tolerant material, we must minimize the strength of this new interaction. We can do this *chemically* and/or *structurally*.

Chemical Criteria

One way to minimize the impact of a new interaction introduced by a defect is to make the host electronic structure robust to this interaction by choosing anioncation pairs that will have strong (dispersive) interactions forming the VBM so that the majority of the dispersion of the new interaction overlaps with states in the host structure.





selected atoms to enable assessment of favorable cation-anion pairs. Pb-I distance (4.4 eV) shown for reference (grey dotted line).

2. Brandt, R. E., Stevanović, V., Ginley, D. S., & Buonassisi, T. (2015). Identifying defect-tolerant semiconductors with high minority-carrier lifetimes: beyond hybrid lead halide perovskites. MRS

Another option is for a compound to have a **crystal structure** that geometrically this can be accomplished through **corner-sharing** coordination polyhedra.





the following simple ratio:

nearest cation-cation distance

nearest cation-anion distance

Preliminary Experimental Validation



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We can measure **carrier lifetime** through **time-resolved**

photoluminescence. Results for three compounds are shown here, and their adherence to the defect tolerance criteria outlined in the table below. It is clear there is some **compensation** or "tradeoff" between the relative influence of the criteria; the nature and extent of this is the subject of ongoing/future work.

Inl	Bil ₃
1.0 eV	6.6 eV
1.5	1.4
Long (1-10 ns)	Short (<1 ns)

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